POZNAN UNIVERSITY OF TECHNOLOGY



EUROPEAN CREDIT TRANSFER AND ACCUMULATION SYSTEM (ECTS)

COURSE DESCRIPTION CARD - SYLLABUS

Course name

Microprocessor data acquisition systems [S2EiT1-SSiU>MSAD]

Course					
Field of study		Year/Semester 1/2 Profile of study general academic			
Electronics and Telecommunications Area of study (specialization) Networks, Systems and Services					
					Level of study second-cycle
Form of study full-time		Requirements elective			
Number of hours					
Lecture 30	Laboratory classes 30	;	Other 0		
Tutorials 0	Projects/seminars 0				
Number of credit points 4,00					
Coordinators	l	Lecturers			
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Prerequisites					
none					
Course objective					
none					
Course-related learning outcomes					
none					
Methods for verifying learning outcome					
Learning outcomes presented above are verified as follows:					
none					

Programme content

none

Course topics

none

Teaching methods

none

Bibliography

none

Breakdown of average student's workload

	Hours	ECTS
Total workload	0	0,00
Classes requiring direct contact with the teacher	0	0,00
Student's own work (literature studies, preparation for laboratory classes/ tutorials, preparation for tests/exam, project preparation)	0	0,00